

bx Feb 7-13 1995.log

```
RCS file: /p/cvsroot/atlas/BOM,v
working file: atlas/BOM
head: 7.0
branch:
locks: strict
access list:
symbolic names:
    b4_rich: 7.0
keyword substitution: kv
total revisions: 1799;  selected revisions: 16
description:
-----
revision 2.47
date: 1995/02/11 00:59:53;  author: brianl;  state: Exp;  lines: +2 -2
Release Target: atlas/leafgen

Generate Depend dependency and list file.
Add rules for xl schematic generation.
-----
revision 2.46
date: 1995/02/11 00:58:52;  author: brianl;  state: Exp;  lines: +2 -2
Release Target: atlas
    Makefile.defs

Add parmsche variables
-----
revision 2.45
date: 1995/02/10 23:21:04;  author: bpw;  state: Exp;  lines: +2 -2
Release Target: atlas/doc
    ccr.ps

Corrected some typos and added long line buffers.
-----
revision 2.44
date: 1995/02/10 22:46:18;  author: bpw;  state: Exp;  lines: +2 -2
Release Target: atlas/doc
    ccr.ps

Moved input mux up half a tick.
-----
revision 2.43
date: 1995/02/10 22:40:54;  author: geert;  state: Exp;  lines: +2 -2
Release Target: atlas/doc

released new layout rules

Geert
-----
revision 2.42
date: 1995/02/10 18:00:54;  author: ong;  state: Exp;  lines: +2 -2
Release Target: atlas/leafgen
    design_crit.ht
    std_cells.ht

Adding Document Standard Cells Design Criterias
-----
revision 2.41
date: 1995/02/09 23:16:31;  author: wingard;  state: Exp;  lines: +2 -2
Release Target: atlas/doc

Clean up some files that we don't need anymore
-----
```

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revision 2.40
date: 1995/02/09 19:00:49; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.39
date: 1995/02/09 05:50:28; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc
layout.ht

Minorfixes

Geert

revision 2.38
date: 1995/02/09 05:41:05; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc

Added standard Cell Layout guidelines

Geert

revision 2.37
date: 1995/02/09 01:47:25; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

New Tobe schematics xvor2n xvor3n xvand2n xvand3n

revision 2.36
date: 1995/02/08 17:57:46; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc

naming convetion document

Geert

revision 2.35
date: 1995/02/08 03:04:52; author: wingard; state: Exp; lines: +2 -2
Release Target: atlas/doc

Update Makefiles and .ht files to correspond to new macro expansion method

revision 2.34
date: 1995/02/08 03:04:19; author: wingard; state: Exp; lines: +2 -2
Release Target: atlas
welcome.ht

Update Makefiles and .ht files to correspond to new macro expansion method

revision 2.33
date: 1995/02/07 16:03:41; author: tom; state: Exp; lines: +2 -2
Release Target: atlas/compass/dummywarts

add atlas dummywarts area

revision 2.32
date: 1995/02/07 15:59:55; author: tom; state: Exp; lines: +5 -1
Release Target: atlas/compass

first cut at atlas compass area

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RCS file: /p/cvsroot/atlas/Makefile.defs,v
working file: atlas/Makefile.defs
head: 1.62
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.62
keyword substitution: kv
total revisions: 62; selected revisions: 2
description:

revision 1.8
date: 1995/02/10 23:46:44; author: brianl; state: Exp; lines: +3 -3
Make -t option part of the PARMSCHE variable default
Add technology file to list of parmsche depends

revision 1.7
date: 1995/02/10 23:20:51; author: brianl; state: Exp; lines: +11 -1
Add parmsche variables
=====

RCS file: /p/cvsroot/atlas/welcome.ht,v
working file: atlas/welcome.ht
head: 2.6
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.6
keyword substitution: kv
total revisions: 6; selected revisions: 1
description:

revision 2.4
date: 1995/02/08 02:35:49; author: wingard; state: Exp; lines: +5 -5
Fix references to match new WebStar - refer to .ht rather than .html
=====

RCS file: /p/cvsroot/atlas/baseplate/Makefile.base,v
working file: atlas/baseplate/Makefile.base
head: 1.50
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.50
keyword substitution: kv
total revisions: 50; selected revisions: 1
description:

revision 1.4
date: 1995/02/12 23:59:53; author: geert; state: Exp; lines: +3 -1
This build a baseplate with a pad-ring
and holes for the custom blocks
=====

Geert
=====

RCS file: /p/cvsroot/atlas/baseplate/chipparms.m4,v
working file: atlas/baseplate/chipparms.m4
head: 1.21
branch:
=====

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locks: strict
access list:
symbolic names:
 b4_rich: 1.21
keyword substitution: kv
total revisions: 21; selected revisions: 3
description:

revision 1.6
date: 1995/02/12 23:59:54; author: geert; state: Exp; lines: +5 -5
This build a baseplate with a pad-ring
and holes for the custom blocks

Geert

revision 1.5
date: 1995/02/11 18:15:27; author: geert; state: Exp; lines: +9 -3
Made a bunch of changes to convert ecl style
baseplate to cmos style. This now builds a very basic baseplate

Geert

revision 1.4
date: 1995/02/08 00:41:52; author: geert; state: Exp; lines: +2 -2
Still trying to build a first baseplate

Geert

=====

RCS file: /p/cvsroot/atlas/baseplate/padring.awk,v
Working file: atlas/baseplate/padring.awk
head: 1.12
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.12
keyword substitution: kv
total revisions: 12; selected revisions: 1
description:

revision 1.2
date: 1995/02/12 23:59:56; author: geert; state: Exp; lines: +19 -9
This build a baseplate with a pad-ring
and holes for the custom blocks

Geert

=====

RCS file: /p/cvsroot/atlas/baseplate/Attic/padtext.awk,v
Working file: atlas/baseplate/padtext.awk
head: 1.9
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 9; selected revisions: 2
description:

revision 1.3
date: 1995/02/12 23:59:58; author: geert; state: Exp; lines: +23 -12
This build a baseplate with a pad-ring

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and holes for the custom blocks

Geert

revision 1.2
date: 1995/02/08 00:41:54; author: geert; state: Exp; lines: +5 -5
Still trying to build a first baseplate

Geert

=====

RCS file: /p/cvsroot/atlas/baseplate/sharedbaseplate.sgen.m4,v
working file: atlas/baseplate/sharedbaseplate.sgen.m4
head: 1.41
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.41
keyword substitution: kv
total revisions: 41; selected revisions: 4
description:

revision 1.7
date: 1995/02/12 23:59:59; author: geert; state: Exp; lines: +5 -7
This build a baseplate with a pad-ring
and holes for the custom blocks

Geert

revision 1.6
date: 1995/02/12 19:10:55; author: geert; state: Exp; lines: +5 -5
Still pretty basic stuff .. converting to microns
adding floorplan, cut out atoms under custom blocks

Geert

revision 1.5
date: 1995/02/11 18:15:37; author: geert; state: Exp; lines: +18 -10
Made a bunch of changes to convert ecl style
baseplate to cmos style. This now builds a very basic baseplate

Geert

revision 1.4
date: 1995/02/08 00:41:56; author: geert; state: Exp; lines: +3 -3
Still trying to build a first baseplate

Geert

=====

RCS file: /p/cvsroot/atlas/compass/BOM,v
working file: atlas/compass/BOM
head: 10.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 10.0
keyword substitution: kv
total revisions: 217; selected revisions: 3
description:
releasebom adding BOM

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revision 2.1
date: 1995/02/07 16:03:33; author: tom; state: Exp; lines: +5 -1
Release Target: atlas/compass/dummywarts

add atlas dummywarts area

revision 2.0
date: 1995/02/07 15:59:46; author: tom; state: Exp; lines: +1 -1
Release Target: atlas/compass

first cut at atlas compass area

revision 1.1
date: 1995/02/07 15:59:40; author: tom; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

=====

RCS file: /p/cvsroot/atlas/compass/vlsi.boo-all,v

Working file: atlas/compass/vlsi.boo-all

head: 1.1

branch:

locks: strict

access list:

symbolic names:

 b4_rich: 1.1

keyword substitution: kv

total revisions: 1; selected revisions: 1

description:

revision 1.1
date: 1995/02/07 15:58:16; author: tom; state: Exp;
first cut at atlas compass area

=====

RCS file: /p/cvsroot/atlas/compass/vlsi.boo-dcell,v

Working file: atlas/compass/vlsi.boo-dcell

head: 1.1

branch:

locks: strict

access list:

symbolic names:

 b4_rich: 1.1

keyword substitution: kv

total revisions: 1; selected revisions: 1

description:

revision 1.1
date: 1995/02/07 15:58:17; author: tom; state: Exp;
first cut at atlas compass area

=====

RCS file: /p/cvsroot/atlas/compass/vlsi.boo-tapeout,v

Working file: atlas/compass/vlsi.boo-tapeout

head: 1.1

branch:

locks: strict

access list:

symbolic names:

 b4_rich: 1.1

keyword substitution: kv

total revisions: 1; selected revisions: 1

description:

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revision 1.1

date: 1995/02/07 15:58:19; author: tom; state: Exp;
first cut at atlas compass area

RCS file: /p/cvsroot/atlas/compass/dummywarts/BOM,v
Working file: atlas/compass/dummywarts/BOM
head: 2.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.0
keyword substitution: kv
total revisions: 2; selected revisions: 2
description:
releasebom adding BOM

revision 2.0

date: 1995/02/07 16:03:22; author: tom; state: Exp; lines: +1 -1
Release Target: atlas/compass/dummywarts

add atlas dummywarts area

revision 1.1

date: 1995/02/07 16:03:13; author: tom; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas/compass/dummywarts/atlasdummywarts.db,v
Working file: atlas/compass/dummywarts/atlasdummywarts.db
head: 1.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.1
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1

date: 1995/02/07 16:02:55; author: tom; state: Exp;
add atlas dummywarts area

RCS file: /p/cvsroot/atlas/compass/dummywarts/vlsi.atr,v
Working file: atlas/compass/dummywarts/vlsi.atr
head: 1.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.1
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1

date: 1995/02/07 16:02:57; author: tom; state: Exp;
add atlas dummywarts area

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RCS file: /p/cvsroot/atlas/compass/dummywarts/vlsi.idx,v
working file: atlas/compass/dummywarts/vlsi.idx
head: 1.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.1
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 16:02:58; author: tom; state: Exp;
add atlas dummywarts area
=====

RCS file: /p/cvsroot/atlas/compass/dummywarts/vlsi.log,v
working file: atlas/compass/dummywarts/vlsi.log
head: 1.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.1
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 16:03:00; author: tom; state: Exp;
add atlas dummywarts area
=====

RCS file: /p/cvsroot/atlas/compass/frame/BOM,v
working file: atlas/compass/frame/BOM
head: 2.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.0
keyword substitution: kv
total revisions: 2; selected revisions: 2
description:
releasebom adding BOM

revision 2.0
date: 1995/02/07 15:59:13; author: tom; state: Exp; lines: +1 -1
Release Target: atlas/compass

first cut at atlas compass area

revision 1.1
date: 1995/02/07 15:59:05; author: tom; state: Exp;
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas/compass/frame/atlasframe.db,v
working file: atlas/compass/frame/atlasframe.db
head: 1.1
branch:

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```
locks: strict
access list:
symbolic names:
  b4_rich: 1.1
keyword substitution: kv
total revisions: 1;      selected revisions: 1
description:
-----
revision 1.1
date: 1995/02/07 15:58:22;  author: tom;  state: Exp;
first cut at atlas compass area
=====
RCS file: /p/cvsroot/atlas/compass/frame/vlsi.atr,v
working file: atlas/compass/frame/vlsi.atr
head: 1.1
branch:
locks: strict
access list:
symbolic names:
  b4_rich: 1.1
keyword substitution: kv
total revisions: 1;      selected revisions: 1
description:
-----
revision 1.1
date: 1995/02/07 15:58:23;  author: tom;  state: Exp;
first cut at atlas compass area
=====
RCS file: /p/cvsroot/atlas/compass/frame/vlsi.idx,v
working file: atlas/compass/frame/vlsi.idx
head: 1.1
branch:
locks: strict
access list:
symbolic names:
  b4_rich: 1.1
keyword substitution: kv
total revisions: 1;      selected revisions: 1
description:
-----
revision 1.1
date: 1995/02/07 15:58:24;  author: tom;  state: Exp;
first cut at atlas compass area
=====
RCS file: /p/cvsroot/atlas/compass/frame/vlsi.log,v
working file: atlas/compass/frame/vlsi.log
head: 1.1
branch:
locks: strict
access list:
symbolic names:
  b4_rich: 1.1
keyword substitution: kv
total revisions: 1;      selected revisions: 1
description:
-----
revision 1.1
date: 1995/02/07 15:58:26;  author: tom;  state: Exp;
first cut at atlas compass area
=====
```

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RCS file: /p/cvsroot/atlas/compass/layouts/BOM,v
working file: atlas/compass/layouts/BOM
head: 75.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 75.0
keyword substitution: kv
total revisions: 278; selected revisions: 2
description:
releasebom adding BOM

revision 2.0
date: 1995/02/07 15:59:31; author: tom; state: Exp; lines: +1 -1
Release Target: atlas/compass

first cut at atlas compass area

revision 1.1
date: 1995/02/07 15:59:24; author: tom; state: Exp;
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas/compass/layouts/atlas.db,v
working file: atlas/compass/layouts/atlas.db
head: 1.136
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.136
keyword substitution: kv
total revisions: 136; selected revisions: 4
description:

revision 1.4
date: 1995/02/10 23:51:05; author: chip; state: Exp; lines: +2 -2
periodic checkin of Fri Feb 10 15:51:04 PST 1995

revision 1.3
date: 1995/02/09 23:10:47; author: chip; state: Exp; lines: +5 -5
periodic checkin of Thu Feb 9 15:10:46 PST 1995

revision 1.2
date: 1995/02/08 18:21:09; author: chip; state: Exp; lines: +9 -4
periodic checkin of Wed Feb 8 10:21:07 PST 1995

revision 1.1
date: 1995/02/07 15:58:29; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas/compass/layouts/emptyatom.ly,v
working file: atlas/compass/layouts/emptyatom.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.3
keyword substitution: kv

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total revisions: 3; selected revisions: 2

description:

revision 2.2

date: 1995/02/09 21:28:24; author: chip; state: Exp; lines: +7 -7

periodic checkin of Thu Feb 9 13:28:21 PST 1995

revision 2.1

date: 1995/02/08 16:05:17; author: chip; state: Exp;

periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/exand1b17nm.ly,v

working file: atlas/compass/layouts/exand1b17nm.ly

head: 2.3

branch:

locks: strict

access list:

symbolic names:

 b4_rich: 2.3

keyword substitution: kv

total revisions: 3; selected revisions: 2

description:

revision 2.2

date: 1995/02/08 16:39:37; author: chip; state: Exp; lines: +138 -123

periodic checkin of Wed Feb 8 08:39:34 PST 1995

revision 2.1

date: 1995/02/08 16:05:19; author: chip; state: Exp;

periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/exnor4nm.ly,v

working file: atlas/compass/layouts/exnor4nm.ly

head: 2.3

branch:

locks: strict

access list:

symbolic names:

 b4_rich: 2.3

keyword substitution: kv

total revisions: 3; selected revisions: 1

description:

revision 2.1

date: 1995/02/08 16:05:21; author: chip; state: Exp;

periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/exvffqm.ly,v

working file: atlas/compass/layouts/exvffqm.ly

head: 2.3

branch:

locks: strict

access list:

symbolic names:

 b4_rich: 2.3

keyword substitution: kv

total revisions: 3; selected revisions: 2

description:

revision 2.2

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date: 1995/02/08 17:06:33; author: chip; state: Exp; lines: +69 -40
periodic checkin of Wed Feb 8 09:06:30 PST 1995

revision 2.1
date: 1995/02/08 16:05:24; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/mos_site.ly,v
Working file: atlas/compass/layouts/mos_site.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.4
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/11 01:48:35; author: chip; state: Exp; lines: +22 -2
periodic checkin of Fri Feb 10 17:48:33 PST 1995

revision 2.1
date: 1995/02/08 01:54:54; author: chip; state: Exp;
periodic checkin of Tue Feb 7 17:54:51 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/padvdd.ly,v
Working file: atlas/compass/layouts/padvdd.ly
head: 2.38
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.38
keyword substitution: kv
total revisions: 38; selected revisions: 1
description:

revision 2.1
date: 1995/02/12 20:00:46; author: chip; state: Exp;
periodic checkin of Sun Feb 12 12:00:44 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/template.ly,v
Working file: atlas/compass/layouts/template.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.4
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/09 21:28:28; author: chip; state: Exp; lines: +20 -21
periodic checkin of Thu Feb 9 13:28:21 PST 1995

revision 2.1

bx Feb 7-13 1995.log
date: 1995/02/08 16:05:26; author: chip; state: Exp;
periodic checkin of wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_10x1.ly,v
Working file: atlas/compass/layouts/uh_10x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:17; author: chip; state: Exp; lines: +3 -3
periodic checkin of wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:27; author: chip; state: Exp;
periodic checkin of wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_11x1.ly,v
Working file: atlas/compass/layouts/uh_11x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:19; author: chip; state: Exp; lines: +4 -4
periodic checkin of wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:29; author: chip; state: Exp;
periodic checkin of wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_12x1.ly,v
Working file: atlas/compass/layouts/uh_12x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:20; author: chip; state: Exp; lines: +3 -3
periodic checkin of wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:31; author: chip; state: Exp;
periodic checkin of wed Feb 8 08:05:10 PST 1995
=====

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RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_13x1.ly,v
working file: atlas/compass/layouts/uh_13x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:21; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:32; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_14x1.ly,v
working file: atlas/compass/layouts/uh_14x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:23; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:34; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_15x1.ly,v
working file: atlas/compass/layouts/uh_15x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:24; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:35; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_16x1.ly,v
working file: atlas/compass/layouts/uh_16x1.ly

bx Feb 7-13 1995.log

head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:25; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1

date: 1995/02/08 16:05:37; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_17x1.ly,v
working file: atlas/compass/layouts/uh_17x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:27; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1

date: 1995/02/08 16:05:38; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_18x1.ly,v
working file: atlas/compass/layouts/uh_18x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:28; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1

date: 1995/02/08 16:05:40; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_19x1.ly,v
working file: atlas/compass/layouts/uh_19x1.ly
head: 2.3
branch:
locks: strict

bx Feb 7-13 1995.log

access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:30; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:41; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_20x1.ly,v
Working file: atlas/compass/layouts/uh_20x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:31; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:43; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_21x1.ly,v
Working file: atlas/compass/layouts/uh_21x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:33; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:44; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_22x1.ly,v
Working file: atlas/compass/layouts/uh_22x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv

bx Feb 7-13 1995.log
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:34; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:46; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_23x1.1y,v
Working file: atlas/compass/layouts/uh_23x1.1y
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:36; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:47; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_24x1.1y,v
Working file: atlas/compass/layouts/uh_24x1.1y
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:37; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:49; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_25x1.1y,v
Working file: atlas/compass/layouts/uh_25x1.1y
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

bx Feb 7-13 1995.log

revision 2.2
date: 1995/02/08 19:17:39; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:50; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_26x1.ly,v
Working file: atlas/compass/layouts/uh_26x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:40; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:52; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_27x1.ly,v
Working file: atlas/compass/layouts/uh_27x1.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:42; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:53; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_28x1.ly,v
Working file: atlas/compass/layouts/uh_28x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:44; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

bx Feb 7-13 1995.log

revision 2.1

date: 1995/02/08 16:05:56; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_29x1.ly,v
working file: atlas/compass/layouts/uh_29x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:47; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

=====

revision 2.1

date: 1995/02/08 16:05:58; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_2x1.ly,v
working file: atlas/compass/layouts/uh_2x1.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:48; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

=====

revision 2.1

date: 1995/02/08 16:05:59; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_30x1.ly,v
working file: atlas/compass/layouts/uh_30x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:50; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

=====

revision 2.1

date: 1995/02/08 16:06:00; author: chip; state: Exp;

bx Feb 7-13 1995.log

periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_3x1.ly,v
working file: atlas/compass/layouts/uh_3x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:52; author: chip; state: Exp; lines: +8 -5
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:03; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_45x1.ly,v
working file: atlas/compass/layouts/uh_45x1.ly
head: 2.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 2.1
date: 1995/02/09 22:36:21; author: chip; state: Exp;
periodic checkin of Thu Feb 9 14:36:18 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_4x1.ly,v
working file: atlas/compass/layouts/uh_4x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:54; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:04; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_5x1.ly,v
working file: atlas/compass/layouts/uh_5x1.ly
head: 2.3
branch:

bx Feb 7-13 1995.log

locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:57; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:06; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_6x1.ly,v
working file: atlas/compass/layouts/uh_6x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:59; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:08; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_7x1.ly,v
working file: atlas/compass/layouts/uh_7x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:18:02; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:09; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_8x1.ly,v
working file: atlas/compass/layouts/uh_8x1.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:

bx Feb 7-13 1995.log

keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:18:06; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:11; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/uh_9x1.ly,v
working file: atlas/compass/layouts/uh_9x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:18:08; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:12; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/uh_grid.ly,v
working file: atlas/compass/layouts/uh_grid.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.4
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 18:14:10; author: chip; state: Exp; lines: +23 -4
periodic checkin of Wed Feb 8 10:14:08 PST 1995

revision 2.1
date: 1995/02/08 16:06:13; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/uh_pwr.ly,v
working file: atlas/compass/layouts/uh_pwr.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 2.3
keyword substitution: kv

bx Feb 7-13 1995.log
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/11 00:07:36; author: chip; state: Exp; lines: +57 -4
periodic checkin of Fri Feb 10 16:07:34 PST 1995

revision 2.1
date: 1995/02/10 22:32:27; author: chip; state: Exp;
periodic checkin of Fri Feb 10 14:32:25 PST 1995
=====

RCS file: /p/cvsroot/atlas/compass/layouts/vlsi.atr,v
Working file: atlas/compass/layouts/vlsi.atr
head: 1.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.1
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:30; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas/compass/layouts/vlsi.idx,v
Working file: atlas/compass/layouts/vlsi.idx
head: 1.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.1
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:32; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas/compass/layouts/vlsi.log,v
Working file: atlas/compass/layouts/vlsi.log
head: 1.3654
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.3654
keyword substitution: kv
total revisions: 3654; selected revisions: 21
description:

revision 1.21
date: 1995/02/12 20:00:49; author: chip; state: Exp; lines: +2 -0
periodic checkin of Sun Feb 12 12:00:44 PST 1995

revision 1.20

bx Feb 7-13 1995.log
date: 1995/02/11 01:48:36; author: chip; state: Exp; lines: +2 -0
periodic checkin of Fri Feb 10 17:48:33 PST 1995

revision 1.19
date: 1995/02/11 01:34:23; author: chip; state: Exp; lines: +1 -0
periodic checkin of Fri Feb 10 17:34:21 PST 1995

revision 1.18
date: 1995/02/11 00:07:37; author: chip; state: Exp; lines: +3 -0
periodic checkin of Fri Feb 10 16:07:34 PST 1995

revision 1.17
date: 1995/02/10 23:49:34; author: chip; state: Exp; lines: +2 -0
periodic checkin of Fri Feb 10 15:49:33 PST 1995

revision 1.16
date: 1995/02/10 23:46:28; author: chip; state: Exp; lines: +2 -0
periodic checkin of Fri Feb 10 15:46:26 PST 1995

revision 1.15
date: 1995/02/10 22:32:29; author: chip; state: Exp; lines: +3 -0
periodic checkin of Fri Feb 10 14:32:25 PST 1995

revision 1.14
date: 1995/02/10 18:27:53; author: chip; state: Exp; lines: +1 -0
periodic checkin of Fri Feb 10 10:27:51 PST 1995

revision 1.13
date: 1995/02/09 23:10:50; author: chip; state: Exp; lines: +2 -0
periodic checkin of Thu Feb 9 15:10:46 PST 1995

revision 1.12
date: 1995/02/09 23:09:00; author: chip; state: Exp; lines: +1 -0
periodic checkin of Thu Feb 9 15:08:58 PST 1995

revision 1.11
date: 1995/02/09 22:36:24; author: chip; state: Exp; lines: +5 -0
periodic checkin of Thu Feb 9 14:36:18 PST 1995

revision 1.10
date: 1995/02/09 22:34:25; author: chip; state: Exp; lines: +3 -0
periodic checkin of Thu Feb 9 14:34:22 PST 1995

revision 1.9
date: 1995/02/09 21:28:30; author: chip; state: Exp; lines: +7 -0
periodic checkin of Thu Feb 9 13:28:21 PST 1995

revision 1.8
date: 1995/02/08 19:18:10; author: chip; state: Exp; lines: +87 -0
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 1.7
date: 1995/02/08 18:21:10; author: chip; state: Exp; lines: +3 -0
periodic checkin of Wed Feb 8 10:21:07 PST 1995

revision 1.6
date: 1995/02/08 18:14:12; author: chip; state: Exp; lines: +2 -0
periodic checkin of Wed Feb 8 10:14:08 PST 1995

revision 1.5
date: 1995/02/08 17:06:36; author: chip; state: Exp; lines: +3 -0
periodic checkin of Wed Feb 8 09:06:30 PST 1995

bx Feb 7-13 1995.log

revision 1.4
date: 1995/02/08 16:39:39; author: chip; state: Exp; lines: +2 -0
periodic checkin of Wed Feb 8 08:39:34 PST 1995

revision 1.3
date: 1995/02/08 16:06:15; author: chip; state: Exp; lines: +105 -0
periodic checkin of Wed Feb 8 08:05:10 PST 1995

revision 1.2
date: 1995/02/08 01:54:57; author: chip; state: Exp; lines: +5 -0
periodic checkin of Tue Feb 7 17:54:51 PST 1995

revision 1.1
date: 1995/02/07 15:58:34; author: tom; state: Exp;
first cut at atlas compass area

=====

RCS file: /p/cvsroot/atlas/compass/layouts/Attic/x1and4n160s.ly,v
working file: atlas/compass/layouts/x1and4n160s.ly
head: 2.7
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 7; selected revisions: 2
description:

revision 2.2
date: 1995/02/09 22:36:28; author: chip; state: Exp; lines: +3 -3
periodic checkin of Thu Feb 9 14:36:18 PST 1995

revision 2.1
date: 1995/02/09 22:34:27; author: chip; state: Exp;
periodic checkin of Thu Feb 9 14:34:22 PST 1995

=====

RCS file: /p/cvsroot/atlas/dcell/cache.dcell,v
working file: atlas/dcell/cache.dcell
head: 1.6
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.6
keyword substitution: kv
total revisions: 6; selected revisions: 1
description:

revision 1.3
date: 1995/02/11 19:30:40; author: geert; state: Exp; lines: +8 -100
Changed from udr's to microns

Geert

=====

RCS file: /p/cvsroot/atlas/doc/BOM,v
working file: atlas/doc/BOM
head: 37.0
branch:
locks: strict
access list:
symbolic names:

bx Feb 7-13 1995.log

b4_rich: 37.0
keyword substitution: kv
total revisions: 83; selected revisions: 13
description:
releasebom adding BOM

revision 25.2
date: 1995/02/10 23:20:52; author: bpw; state: Exp; lines: +2 -2
Release Target: atlas/doc
ccr.ps

Corrected some typos and added long line buffers.

revision 25.1
date: 1995/02/10 22:46:08; author: bpw; state: Exp; lines: +2 -2
Release Target: atlas/doc
ccr.ps

Moved input mux up half a tick.

revision 25.0
date: 1995/02/10 22:40:42; author: geert; state: Exp; lines: +1 -1
Release Target: atlas/doc

released new layout rules

Geert

revision 24.1
date: 1995/02/10 22:40:34; author: geert; state: Exp; lines: +2 -2
releasebom: File needs to be up-to-date to use commit -r

revision 24.0
date: 1995/02/09 23:16:21; author: wingard; state: Exp; lines: +1 -1
Release Target: atlas/doc

Clean up some files that we don't need anymore

revision 23.2
date: 1995/02/09 23:16:15; author: wingard; state: Exp; lines: +4 -6
releasebom: File needs to be up-to-date to use commit -r

revision 23.1
date: 1995/02/09 05:50:17; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc
layout.ht

Minorfixes

Geert

revision 23.0
date: 1995/02/09 05:40:52; author: geert; state: Exp; lines: +1 -1
Release Target: atlas/doc

Added standard Cell Layout guidelines

Geert

revision 22.1
date: 1995/02/09 05:40:43; author: geert; state: Exp; lines: +2 -2
releasebom: File needs to be up-to-date to use commit -r

bx Feb 7-13 1995.log

revision 22.0
date: 1995/02/08 17:57:37; author: geert; state: Exp; lines: +1 -1
Release Target: atlas/doc

naming convection document

Geert

revision 21.1
date: 1995/02/08 17:57:31; author: geert; state: Exp; lines: +3 -2
releasebom: File needs to be up-to-date to use commit -r

revision 21.0
date: 1995/02/08 03:04:42; author: wingard; state: Exp; lines: +1 -1
Release Target: atlas/doc

Update Makefiles and .ht files to correspond to new macro expansion method

revision 20.1
date: 1995/02/08 03:04:35; author: wingard; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas/doc/Attic/Makefile,v
working file: atlas/doc/Makefile
head: 1.13
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 13; selected revisions: 1
description:

revision 1.13
date: 1995/02/08 02:36:02; author: wingard; state: Exp; lines: +2 -3
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas/doc/ccr.ps,v
working file: atlas/doc/ccr.ps
head: 23.3
branch:
locks: strict
access list:
symbolic names:
b4_rich: 23.3
keyword substitution: kv
total revisions: 3; selected revisions: 3
description:

revision 23.3
date: 1995/02/10 23:20:30; author: bpw; state: Exp; lines: +188 -60
Corrected some typos and added long line buffers.

revision 23.2
date: 1995/02/10 22:44:55; author: bpw; state: Exp; lines: +500 -465
Moved mux up half a tick back to make it easier to make timing.

revision 23.1
date: 1995/02/09 23:15:32; author: wingard; state: Exp;
Clean up some files that we don't need anymore

bx Feb 7-13 1995.log

RCS file: /p/cvsroot/atlas/doc/cr.ht,v
working file: atlas/doc/cr.ht
head: 5.6
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 5.6
keyword substitution: kv
total revisions: 6; selected revisions: 3
description:

revision 5.6
date: 1995/02/09 23:15:34; author: wingard; state: Exp; lines: +3 -3
Clean up some files that we don't need anymore

revision 5.5
date: 1995/02/08 17:57:01; author: geert; state: Exp; lines: +2 -1
merge with checked in stuff
Geert

revision 5.4
date: 1995/02/08 02:36:03; author: wingard; state: Exp; lines: +3 -3
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas/doc/decisions.ht,v
working file: atlas/doc/decisions.ht
head: 1.8
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.8
keyword substitution: kv
total revisions: 8; selected revisions: 1
description:

revision 1.7
date: 1995/02/08 02:36:05; author: wingard; state: Exp; lines: +5 -5
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas/doc/layout.ht,v
working file: atlas/doc/layout.ht
head: 11.18
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 11.18
keyword substitution: kv
total revisions: 18; selected revisions: 5
description:

revision 11.12
date: 1995/02/10 22:39:18; author: geert; state: Exp; lines: +4 -1
Added rule on standard cell layout

Geert

revision 11.11

bx Feb 7-13 1995.log
date: 1995/02/09 23:15:36; author: wingard; state: Exp; lines: +6 -4
Clean up some files that we don't need anymore

revision 11.10
date: 1995/02/09 05:49:22; author: geert; state: Exp; lines: +2 -2
minor changes

Geert

revision 11.9
date: 1995/02/09 05:39:40; author: geert; state: Exp; lines: +12 -1
Added standard cell layout rules

Geetr

revision 11.8
date: 1995/02/08 02:36:06; author: wingard; state: Exp; lines: +3 -3
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas/doc/Attic/name_conv.ht,v
working file: atlas/doc/name_conv.ht
head: 21.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 21.1
date: 1995/02/08 17:52:44; author: geert; state: Exp;
Naming Convention document

Geert

RCS file: /p/cvsroot/atlas/ged/BOM,v
working file: atlas/ged/BOM
head: 22.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 22.0
keyword substitution: kv
total revisions: 654; selected revisions: 2
description:

revision 4.2
date: 1995/02/09 19:00:39; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 4.1
date: 1995/02/09 01:47:15; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

bx Feb 7-13 1995.log
RCS file: /p/cvsroot/atlas/ged/xv/BOM,v
working file: atlas/ged/xv/BOM
head: 33.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 33.0
keyword substitution: kv
total revisions: 102; selected revisions: 4
description:
releasebom adding BOM

revision 5.0
date: 1995/02/09 19:00:29; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 4.1
date: 1995/02/09 19:00:22; author: ong; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r

revision 4.0
date: 1995/02/09 01:47:05; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 3.1
date: 1995/02/09 01:46:59; author: ong; state: Exp; lines: +22 -12
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand2n/Attic/BOM,v
working file: atlas/ged/xv/xvand2n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 4
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 18:57:05; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1
date: 1995/02/09 18:56:58; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0
date: 1995/02/09 01:11:09; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1

```
bx Feb 7-13 1995.log
date: 1995/02/09 01:11:02; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r
=====
RCS file: /p/cvsroot/atlas/ged/xv/xvand2n/Attic/body.1.1,v
working file: atlas/ged/xv/xvand2n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:
-----
revision 1.2
date: 1995/02/09 18:53:31; author: ong; state: Exp; lines: +4 -12
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.
-----
revision 1.1
date: 1995/02/09 01:08:40; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====
RCS file: /p/cvsroot/atlas/ged/xv/xvand2n/Attic/body.2.1,v
working file: atlas/ged/xv/xvand2n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:
-----
revision 1.2
date: 1995/02/09 18:53:34; author: ong; state: Exp; lines: +12 -20
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.
-----
revision 1.1
date: 1995/02/09 01:08:42; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====
RCS file: /p/cvsroot/atlas/ged/xv/xvand2n/Attic/spice.1.1,v
working file: atlas/ged/xv/xvand2n/spice.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:
-----
revision 1.1
date: 1995/02/09 01:08:44; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
```

bx Feb 7-13 1995.log
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/ged/xv/xvand2n/Attic/spice_cn.1.1,v
working file: atlas/ged/xv/xvand2n/spice_cn.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:08:46; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/ged/xv/xvand3n/Attic/BOM,v
working file: atlas/ged/xv/xvand3n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 4
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 18:57:26; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1
date: 1995/02/09 18:57:19; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0
date: 1995/02/09 01:11:26; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New Lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1
date: 1995/02/09 01:11:20; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas/ged/xv/xvand3n/Attic/body.1.1,v
working file: atlas/ged/xv/xvand3n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2

bx Feb 7-13 1995.log

description:

revision 1.2

date: 1995/02/09 18:53:55; author: ong; state: Exp; lines: +14 -14
Fixed Beta Parameter on Body.

revision 1.1

date: 1995/02/09 01:08:53; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand3n/Attic/body.2.1,v
Working file: atlas/ged/xv/xvand3n/body.2.1

head: 1.3

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 3; selected revisions: 2

description:

revision 1.2

date: 1995/02/09 18:53:56; author: ong; state: Exp; lines: +15 -15
Fixed Beta Parameter on Body.

revision 1.1

date: 1995/02/09 01:08:55; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand3n/Attic/spice.1.1,v

Working file: atlas/ged/xv/xvand3n/spice.1.1

head: 1.1

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 1; selected revisions: 1

description:

revision 1.1

date: 1995/02/09 01:08:56; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand3n/Attic/spice_cn.1.1,v

Working file: atlas/ged/xv/xvand3n/spice_cn.1.1

head: 1.1

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 1; selected revisions: 1

description:

bx Feb 7-13 1995.log

revision 1.1
date: 1995/02/09 01:08:59; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand4n/Attic/BOM,v
working file: atlas/ged/xv/xvand4n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 2
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 01:11:49; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv
New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 2.1
date: 1995/02/09 01:11:42; author: ong; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand4n/Attic/body.1.1,v
working file: atlas/ged/xv/xvand4n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 23:17:17; author: tom; state: Exp; lines: +18 -17
add PARMS properties to bodies of static gates
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand4n/Attic/body.2.1,v
working file: atlas/ged/xv/xvand4n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 23:17:20; author: tom; state: Exp; lines: +18 -17
add PARMS properties to bodies of static gates
=====

bx Feb 7-13 1995.log
RCS file: /p/cvsroot/atlas/ged/xv/xvand4n/Attic/spice.1.1,v
working file: atlas/ged/xv/xvand4n/spice.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:07; author: ong; state: Exp; lines: +29 -29
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvand4n/Attic/spice_cn.1.1,v
working file: atlas/ged/xv/xvand4n/spice_cn.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:09; author: ong; state: Exp; lines: +18 -18
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvbufn/BOM,v
working file: atlas/ged/xv/xvbufn/BOM
head: 6.0
branch:
locks: strict
access list:
symbolic names:
b4_rich: 6.0
keyword substitution: kv
total revisions: 10; selected revisions: 2
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 01:12:09; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv
New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 2.1
date: 1995/02/09 01:12:03; author: ong; state: Exp; lines: +4 -4
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvbufn/body.1.1,v
working file: atlas/ged/xv/xvbufn/body.1.1
head: 1.4

bx Feb 7-13 1995.log

branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.4
keyword substitution: kv
total revisions: 4; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 23:17:24; author: tom; state: Exp; lines: +3 -2
add PARMS properties to bodies of static gates
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvbufn/spice.1.1,v
Working file: atlas/ged/xv/xvbufn/spice.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.3
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:22; author: ong; state: Exp; lines: +145 -145
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvbufn/spice_cn.1.1,v
Working file: atlas/ged/xv/xvbufn/spice_cn.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 1.3
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:23; author: ong; state: Exp; lines: +35 -35
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvor2n/Attic/BOM,v
Working file: atlas/ged/xv/xvor2n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 4
description:
releasebom adding BOM

bx Feb 7-13 1995.log

revision 3.0
date: 1995/02/09 18:59:35; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1
date: 1995/02/09 18:59:28; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0
date: 1995/02/09 01:13:43; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New Lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1
date: 1995/02/09 01:13:36; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas/ged/xv/xvor2n/Attic/body.1.1,v
Working file: atlas/ged/xv/xvor2n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:54:15; author: ong; state: Exp; lines: +14 -14
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.

revision 1.1
date: 1995/02/09 01:09:30; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/ged/xv/xvor2n/Attic/body.2.1,v
Working file: atlas/ged/xv/xvor2n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:54:17; author: ong; state: Exp; lines: +14 -14
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.

revision 1.1
date: 1995/02/09 01:09:32; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit

bx Feb 7-13 1995.log

Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/ged/xv/xvor2n/Attic/spice.1.1,v
Working file: atlas/ged/xv/xvor2n/spice.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:09:33; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/ged/xv/xvor2n/Attic/spice_cn.1.1,v
Working file: atlas/ged/xv/xvor2n/spice_cn.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:09:35; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/ged/xv/xvor3n/Attic/BOM,v
Working file: atlas/ged/xv/xvor3n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 4
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 18:59:58; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1
date: 1995/02/09 18:59:50; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0
date: 1995/02/09 01:14:00; author: ong; state: Exp; lines: +1 -1

bx Feb 7-13 1995.log

Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1

date: 1995/02/09 01:13:53; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

=====

RCS file: /p/cvsroot/atlas/ged/xv/xvor3n/Attic/body.1.1,v
working file: atlas/ged/xv/xvor3n/body.1.1

head: 1.3

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 3; selected revisions: 2

description:

revision 1.2

date: 1995/02/09 18:54:56; author: ong; state: Exp; lines: +17 -17
Fixed Beta Parameter on Body.

=====

revision 1.1

date: 1995/02/09 01:09:41; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

=====

RCS file: /p/cvsroot/atlas/ged/xv/xvor3n/Attic/body.2.1,v

working file: atlas/ged/xv/xvor3n/body.2.1

head: 1.3

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 3; selected revisions: 2

description:

revision 1.2

date: 1995/02/09 18:54:59; author: ong; state: Exp; lines: +17 -17
Fixed Beta Parameter on Body.

=====

revision 1.1

date: 1995/02/09 01:09:42; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

=====

RCS file: /p/cvsroot/atlas/ged/xv/xvor3n/Attic/spice.1.1,v

working file: atlas/ged/xv/xvor3n/spice.1.1

head: 1.1

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 1; selected revisions: 1

description:

bx Feb 7-13 1995.log

revision 1.1
date: 1995/02/09 01:09:45; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvor3n/Attic/spice_cn.1.1,v
Working file: atlas/ged/xv/xvor3n/spice_cn.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:09:47; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvor4n/Attic/BOM,v
Working file: atlas/ged/xv/xvor4n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 2
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 01:14:20; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New Lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 2.1
date: 1995/02/09 01:14:14; author: ong; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas/ged/xv/xvor4n/Attic/body.1.1,v
Working file: atlas/ged/xv/xvor4n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 23:17:30; author: tom; state: Exp; lines: +1 -0
add PARMS properties to bodies of static gates

bx Feb 7-13 1995.log

RCS file: /p/cvsroot/atlas/ged/xv/xvor4n/Attic/body.2.1,v
working file: atlas/ged/xv/xvor4n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 23:17:31; author: tom; state: Exp; lines: +20 -19
add PARMS properties to bodies of static gates

RCS file: /p/cvsroot/atlas/ged/xv/xvor4n/Attic/spice.1.1,v
working file: atlas/ged/xv/xvor4n/spice.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:52; author: ong; state: Exp; lines: +50 -50
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/ged/xv/xvor4n/Attic/spice_cn.1.1,v
working file: atlas/ged/xv/xvor4n/spice_cn.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:54; author: ong; state: Exp; lines: +25 -25
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas/leafgen/BOM,v
working file: atlas/leafgen/BOM
head: 45.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 45.0
keyword substitution: kv

```
          bx Feb 7-13 1995.log
total revisions: 129;  selected revisions: 3
description:
releasebom adding BOM
-----
revision 4.0
date: 1995/02/11 00:59:44;  author: brianl;  state: Exp;  lines: +1 -1
Release Target: atlas/leafgen
```

Generate Depend dependency and list file.
Add rules for xl schematic generation.

```
-----
revision 3.2
date: 1995/02/11 00:59:38;  author: brianl;  state: Exp;  lines: +5 -6
releasebom: File needs to be up-to-date to use commit -r
```

```
-----
revision 3.1
date: 1995/02/10 18:00:44;  author: ong;  state: Exp;  lines: +6 -2
Release Target: atlas/leafgen
  design_crit.ht
  std_cells.ht
```

Adding Document Standard Cells Design Criterias

```
RCS file: /p/cvsroot/atlas/leafgen/Makefile,v
Working file: atlas/leafgen/Makefile
head: 1.33
branch:
locks: strict
access list:
symbolic names:
  b4_rich: 1.33
keyword substitution: kv
total revisions: 33;  selected revisions: 4
description:
```

```
-----
revision 1.5
date: 1995/02/10 23:48:44;  author: brianl;  state: Exp;  lines: +1 -2
parmsche -t option is now part of the default variable
```

```
-----
revision 1.4
date: 1995/02/10 23:20:22;  author: brianl;  state: Exp;  lines: +73 -3
Generate Depend dependency and list file.
Add rules for xl schematic generation.
```

```
-----
revision 1.3
date: 1995/02/08 02:35:58;  author: wingard;  state: Exp;  lines: +2 -2
Fix references to match new WebStar - refer to .ht rather than .html
```

```
-----
revision 1.2
date: 1995/02/08 00:15:25;  author: ong;  state: Exp;  lines: +2 -2
Added design criteria document.  currently the only access is from CMOS STD
CELL document.
```

```
RCS file: /p/cvsroot/atlas/leafgen/Attic/design_crit.ht,v
Working file: atlas/leafgen/design_crit.ht
head: 3.4
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
```

bx Feb 7-13 1995.log
total revisions: 4; selected revisions: 2
description:

revision 3.2
date: 1995/02/10 17:54:46; author: ong; state: Exp; lines: +6 -6
New doc file descibing design criterias of standard cells

revision 3.1
date: 1995/02/08 00:15:27; author: ong; state: Exp;
Added design criteria document. currently the only access is from CMOS STD
CELL document.
=====

RCS file: /p/cvsroot/atlas/leafgen/makeDepend,v
working file: atlas/leafgen/makeDepend
head: 3.15
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 3.15
keyword substitution: kv
total revisions: 15; selected revisions: 1
description:

revision 3.1
date: 1995/02/10 23:20:24; author: brianl; state: Exp;
Generate Depend dependency and list file.
Add rules for xl schematic generation.
=====

RCS file: /p/cvsroot/atlas/leafgen/parmsche-tech.tcl,v
working file: atlas/leafgen/parmsche-tech.tcl
head: 3.11
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 3.11
keyword substitution: kv
total revisions: 11; selected revisions: 1
description:

revision 3.1
date: 1995/02/10 20:17:21; author: tom; state: Exp;
new technology definition file
=====

RCS file: /p/cvsroot/atlas/leafgen/Attic/std_cells.ht,v
working file: atlas/leafgen/std_cells.ht
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 00:15:29; author: ong; state: Exp; lines: +2 -2
Added design criteria document. currently the only access is from CMOS STD
CELL document.

bx Feb 7-13 1995.log

```
RCS file: /p/cvsroot/atlas2/BOM,v
working file: atlas2/BOM
head: 201.2755
branch:
locks: strict
access list:
symbolic names:
      b4_rich: 201.2700
keyword substitution: kv
total revisions: 4418;  selected revisions: 16
description:
-----
revision 2.47
date: 1995/02/11 00:59:53;  author: brianl;  state: Exp;  lines: +2 -2
Release Target: atlas/leafgen

Generate Depend dependency and list file.
Add rules for xl schematic generation.

-----
revision 2.46
date: 1995/02/11 00:58:52;  author: brianl;  state: Exp;  lines: +2 -2
Release Target: atlas
      Makefile.defs

Add parmsche variables
-----
revision 2.45
date: 1995/02/10 23:21:04;  author: bpw;  state: Exp;  lines: +2 -2
Release Target: atlas/doc
      ccr.ps

Corrected some typos and added long line buffers.

-----
revision 2.44
date: 1995/02/10 22:46:18;  author: bpw;  state: Exp;  lines: +2 -2
Release Target: atlas/doc
      ccr.ps

Moved input mux up half a tick.

-----
revision 2.43
date: 1995/02/10 22:40:54;  author: geert;  state: Exp;  lines: +2 -2
Release Target: atlas/doc

released new layout rules

Geert
-----
revision 2.42
date: 1995/02/10 18:00:54;  author: ong;  state: Exp;  lines: +2 -2
Release Target: atlas/leafgen
      design_crit.ht
      std_cells.ht

Adding Document Standard Cells Design Criterias
-----
revision 2.41
date: 1995/02/09 23:16:31;  author: wingard;  state: Exp;  lines: +2 -2
Release Target: atlas/doc

Clean up some files that we don't need anymore
```

bx Feb 7-13 1995.log

revision 2.40
date: 1995/02/09 19:00:49; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.39
date: 1995/02/09 05:50:28; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc
layout.ht

Minorfixes

Geert

revision 2.38
date: 1995/02/09 05:41:05; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc

Added standard Cell Layout guidelines

Geert

revision 2.37
date: 1995/02/09 01:47:25; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 2.36
date: 1995/02/08 17:57:46; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc

naming convention document

Geert

revision 2.35
date: 1995/02/08 03:04:52; author: wingard; state: Exp; lines: +2 -2
Release Target: atlas/doc

Update Makefiles and .ht files to correspond to new macro expansion method

revision 2.34
date: 1995/02/08 03:04:19; author: wingard; state: Exp; lines: +2 -2
Release Target: atlas
welcome.ht

Update Makefiles and .ht files to correspond to new macro expansion method

revision 2.33
date: 1995/02/07 16:03:41; author: tom; state: Exp; lines: +2 -2
Release Target: atlas/compass/dummywarts

add atlas dummywarts area

revision 2.32
date: 1995/02/07 15:59:55; author: tom; state: Exp; lines: +5 -1
Release Target: atlas/compass

first cut at atlas compass area

bx Feb 7-13 1995.log

RCS file: /p/cvsroot/atlas2/Makefile.defs,v
Working file: atlas2/Makefile.defs
head: 200.39
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.39
keyword substitution: kv
total revisions: 99; selected revisions: 2
description:

revision 1.8
date: 1995/02/10 23:46:44; author: brianl; state: Exp; lines: +3 -3
Make -t option part of the PARMSCHE variable default
Add technology file to list of parmsche depends

revision 1.7
date: 1995/02/10 23:20:51; author: brianl; state: Exp; lines: +11 -1
Add parmsche variables
=====

RCS file: /p/cvsroot/atlas2/welcome.ht,v
Working file: atlas2/welcome.ht
head: 200.4
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.4
keyword substitution: kv
total revisions: 11; selected revisions: 1
description:

revision 2.4
date: 1995/02/08 02:35:49; author: wingard; state: Exp; lines: +5 -5
Fix references to match new WebStar - refer to .ht rather than .html
=====

RCS file: /p/cvsroot/atlas2/baseplate/Makefile.base,v
Working file: atlas2/baseplate/Makefile.base
head: 200.27
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.27
keyword substitution: kv
total revisions: 76; selected revisions: 1
description:

revision 1.4
date: 1995/02/12 23:59:53; author: geert; state: Exp; lines: +3 -1
This build a baseplate with a pad-ring
and holes for the custom blocks

Geert
=====

RCS file: /p/cvsroot/atlas2/baseplate/chipparms.m4,v
Working file: atlas2/baseplate/chipparms.m4
head: 200.13

bx Feb 7-13 1995.log

branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.13
keyword substitution: kv
total revisions: 35; selected revisions: 3
description:

revision 1.6
date: 1995/02/12 23:59:54; author: geert; state: Exp; lines: +5 -5
This build a baseplate with a pad-ring
and holes for the custom blocks

Geert

revision 1.5
date: 1995/02/11 18:15:27; author: geert; state: Exp; lines: +9 -3
Made a bunch of changes to convert ecl style
baseplate to cmos style. This now builds a very basic baseplate

Geert

revision 1.4
date: 1995/02/08 00:41:52; author: geert; state: Exp; lines: +2 -2
Still trying to build a first baseplate

Geert

=====

RCS file: /p/cvsroot/atlas2/baseplate/padring.awk,v
Working file: atlas2/baseplate/padring.awk
head: 200.11
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.11
keyword substitution: kv
total revisions: 24; selected revisions: 1
description:

revision 1.2
date: 1995/02/12 23:59:56; author: geert; state: Exp; lines: +19 -9
This build a baseplate with a pad-ring
and holes for the custom blocks

Geert

=====

RCS file: /p/cvsroot/atlas2/baseplate/Attic/padtext.awk,v
Working file: atlas2/baseplate/padtext.awk
head: 1.9
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 9; selected revisions: 2
description:

revision 1.3
date: 1995/02/12 23:59:58; author: geert; state: Exp; lines: +23 -12

bx Feb 7-13 1995.log

This build a baseplate with a pad-ring
and holes for the custom blocks

Geert

revision 1.2
date: 1995/02/08 00:41:54; author: geert; state: Exp; lines: +5 -5
Still trying to build a first baseplate

Geert

=====

RCS file: /p/cvsroot/atlas2/baseplate/Attic/sharedbaseplate.sgen.m4,v
working file: atlas2/baseplate/sharedbaseplate.sgen.m4
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 42; selected revisions: 4
description:

revision 1.7
date: 1995/02/12 23:59:59; author: geert; state: Exp; lines: +5 -7
This build a baseplate with a pad-ring
and holes for the custom blocks

Geert

revision 1.6
date: 1995/02/12 19:10:55; author: geert; state: Exp; lines: +5 -5
Still pretty basic stuff .. converting to microns
adding floorplan, cut out atoms under custom blocks

Geert

revision 1.5
date: 1995/02/11 18:15:37; author: geert; state: Exp; lines: +18 -10
Made a bunch of changes to convert ecl style
baseplate to cmos style. This now builds a very basic baseplate

Geert

revision 1.4
date: 1995/02/08 00:41:56; author: geert; state: Exp; lines: +3 -3
Still trying to build a first baseplate

Geert

=====

RCS file: /p/cvsroot/atlas2/compass/BOM,v
working file: atlas2/compass/BOM
head: 203.329
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 203.329
keyword substitution: kv
total revisions: 532; selected revisions: 3
description:
releasebom adding BOM

bx Feb 7-13 1995.log

revision 2.1
date: 1995/02/07 16:03:33; author: tom; state: Exp; lines: +5 -1
Release Target: atlas/compass/dummywarts

add atlas dummywarts area

revision 2.0
date: 1995/02/07 15:59:46; author: tom; state: Exp; lines: +1 -1
Release Target: atlas/compass

first cut at atlas compass area

revision 1.1
date: 1995/02/07 15:59:40; author: tom; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas2/compass/vlsi.boo-all,v
working file: atlas2/compass/vlsi.boo-all
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:16; author: tom; state: Exp;
first cut at atlas compass area

RCS file: /p/cvsroot/atlas2/compass/vlsi.boo-dcell,v
working file: atlas2/compass/vlsi.boo-dcell
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:17; author: tom; state: Exp;
first cut at atlas compass area

RCS file: /p/cvsroot/atlas2/compass/vlsi.boo-tapeout,v
working file: atlas2/compass/vlsi.boo-tapeout
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

bx Feb 7-13 1995.log

```
-----  
revision 1.1  
date: 1995/02/07 15:58:19; author: tom; state: Exp;  
first cut at atlas compass area  
=====  
RCS file: /p/cvsroot/atlas2/compass/dummywarts/BOM,v  
working file: atlas2/compass/dummywarts/BOM  
head: 201.0  
branch:  
locks: strict  
access list:  
symbolic names:  
    b4_rich: 201.0  
keyword substitution: kv  
total revisions: 5; selected revisions: 2  
description:  
releasebom adding BOM  
-----  
revision 2.0  
date: 1995/02/07 16:03:22; author: tom; state: Exp; lines: +1 -1  
Release Target: atlas/compass/dummywarts  
add atlas dummywarts area  
-----  
revision 1.1  
date: 1995/02/07 16:03:13; author: tom; state: Exp;  
releasebom: File needs to be up-to-date to use commit -r  
=====  
RCS file: /p/cvsroot/atlas2/compass/dummywarts/atlasdummywarts.db,v  
working file: atlas2/compass/dummywarts/atlasdummywarts.db  
head: 200.0  
branch:  
locks: strict  
access list:  
symbolic names:  
    b4_rich: 200.0  
keyword substitution: kv  
total revisions: 2; selected revisions: 1  
description:  
-----  
revision 1.1  
date: 1995/02/07 16:02:55; author: tom; state: Exp;  
add atlas dummywarts area  
=====  
RCS file: /p/cvsroot/atlas2/compass/dummywarts/vlsi.atr,v  
working file: atlas2/compass/dummywarts/vlsi.atr  
head: 200.0  
branch:  
locks: strict  
access list:  
symbolic names:  
    b4_rich: 200.0  
keyword substitution: kv  
total revisions: 2; selected revisions: 1  
description:  
-----  
revision 1.1  
date: 1995/02/07 16:02:57; author: tom; state: Exp;  
add atlas dummywarts area  
=====
```

bx Feb 7-13 1995.log

RCS file: /p/cvsroot/atlas2/compass/dummywarts/vlsi.idx,v
working file: atlas2/compass/dummywarts/vlsi.idx
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 16:02:58; author: tom; state: Exp;
add atlas dummywarts area
=====

RCS file: /p/cvsroot/atlas2/compass/dummywarts/vlsi.log,v
working file: atlas2/compass/dummywarts/vlsi.log
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 16:03:00; author: tom; state: Exp;
add atlas dummywarts area
=====

RCS file: /p/cvsroot/atlas2/compass/frame/BOM,v
working file: atlas2/compass/frame/BOM
head: 201.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 201.0
keyword substitution: kv
total revisions: 5; selected revisions: 2
description:
releasebom adding BOM

revision 2.0
date: 1995/02/07 15:59:13; author: tom; state: Exp; lines: +1 -1
Release Target: atlas/compass
=====

first cut at atlas compass area

revision 1.1
date: 1995/02/07 15:59:05; author: tom; state: Exp;
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas2/compass/frame/atlasframe.db,v
working file: atlas2/compass/frame/atlasframe.db
head: 200.0
branch:
=====

bx Feb 7-13 1995.log

locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:22; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas2/compass/frame/vlsi.atr,v
working file: atlas2/compass/frame/vlsi.atr
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:23; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas2/compass/frame/vlsi.idx,v
working file: atlas2/compass/frame/vlsi.idx
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:24; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas2/compass/frame/vlsi.log,v
working file: atlas2/compass/frame/vlsi.log
head: 200.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:26; author: tom; state: Exp;
first cut at atlas compass area
=====

bx Feb 7-13 1995.log

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/BOM,v
Working file: atlas2/compass/layouts/BOM
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 248; selected revisions: 2
description:
releasebom adding BOM

revision 2.0
date: 1995/02/07 15:59:31; author: tom; state: Exp; lines: +1 -1
Release Target: atlas/compass

first cut at atlas compass area

revision 1.1
date: 1995/02/07 15:59:24; author: tom; state: Exp;
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/atlas.db,v
Working file: atlas2/compass/layouts/atlas.db
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 133; selected revisions: 4
description:

revision 1.4
date: 1995/02/10 23:51:05; author: chip; state: Exp; lines: +2 -2
periodic checkin of Fri Feb 10 15:51:04 PST 1995

revision 1.3
date: 1995/02/09 23:10:47; author: chip; state: Exp; lines: +5 -5
periodic checkin of Thu Feb 9 15:10:46 PST 1995

revision 1.2
date: 1995/02/08 18:21:09; author: chip; state: Exp; lines: +9 -4
periodic checkin of Wed Feb 8 10:21:07 PST 1995

revision 1.1
date: 1995/02/07 15:58:29; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/emptyatom.ly,v
Working file: atlas2/compass/layouts/emptyatom.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

bx Feb 7-13 1995.log

revision 2.2
date: 1995/02/09 21:28:24; author: chip; state: Exp; lines: +7 -7
periodic checkin of Thu Feb 9 13:28:21 PST 1995

revision 2.1
date: 1995/02/08 16:05:17; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/exand1b17nm.ly,v
working file: atlas2/compass/layouts/exand1b17nm.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 16:39:37; author: chip; state: Exp; lines: +138 -123
periodic checkin of Wed Feb 8 08:39:34 PST 1995

revision 2.1
date: 1995/02/08 16:05:19; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/exnor4nm.ly,v
working file: atlas2/compass/layouts/exnor4nm.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 1
description:

revision 2.1
date: 1995/02/08 16:05:21; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/exvffqm.ly,v
working file: atlas2/compass/layouts/exvffqm.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 17:06:33; author: chip; state: Exp; lines: +69 -40
periodic checkin of Wed Feb 8 09:06:30 PST 1995

revision 2.1
date: 1995/02/08 16:05:24; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

bx Feb 7-13 1995.log

```
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/mos_site.ly,v
working file: atlas2/compass/layouts/mos_site.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 5;      selected revisions: 2
description:
-----
revision 2.2
date: 1995/02/11 01:48:35;  author: chip;  state: Exp;  lines: +22 -2
periodic checkin of Fri Feb 10 17:48:33 PST 1995
-----
revision 2.1
date: 1995/02/08 01:54:54;  author: chip;  state: Exp;
periodic checkin of Tue Feb  7 17:54:51 PST 1995
=====
```

```
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/padvdd.ly,v
working file: atlas2/compass/layouts/padvdd.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 39;      selected revisions: 1
description:
-----
revision 2.1
date: 1995/02/12 20:00:46;  author: chip;  state: Exp;
periodic checkin of Sun Feb 12 12:00:44 PST 1995
=====
```

```
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/template.ly,v
working file: atlas2/compass/layouts/template.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 5;      selected revisions: 2
description:
-----
revision 2.2
date: 1995/02/09 21:28:28;  author: chip;  state: Exp;  lines: +20 -21
periodic checkin of Thu Feb  9 13:28:21 PST 1995
-----
revision 2.1
date: 1995/02/08 16:05:26;  author: chip;  state: Exp;
periodic checkin of Wed Feb  8 08:05:10 PST 1995
=====
```

```
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_10x1.ly,v
working file: atlas2/compass/layouts/uh_10x1.ly
head: 2.3
branch:
locks: strict
=====
```

bx Feb 7-13 1995.log

access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:17; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:27; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_11x1.ly,v
working file: atlas2/compass/layouts/uh_11x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:19; author: chip; state: Exp; lines: +4 -4
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:29; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_12x1.ly,v
working file: atlas2/compass/layouts/uh_12x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:20; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:31; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_13x1.ly,v
working file: atlas2/compass/layouts/uh_13x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv

bx Feb 7-13 1995.log
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:21; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:32; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_14x1.ly,v
working file: atlas2/compass/layouts/uh_14x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:23; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:34; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_15x1.ly,v
working file: atlas2/compass/layouts/uh_15x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:24; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:35; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_16x1.ly,v
working file: atlas2/compass/layouts/uh_16x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

bx Feb 7-13 1995.log

revision 2.2
date: 1995/02/08 19:17:25; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:37; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_17x1.ly,v
working file: atlas2/compass/layouts/uh_17x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:27; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:38; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_18x1.ly,v
working file: atlas2/compass/layouts/uh_18x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:28; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:40; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_19x1.ly,v
working file: atlas2/compass/layouts/uh_19x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:30; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

bx Feb 7-13 1995.log

revision 2.1

date: 1995/02/08 16:05:41; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_20x1.ly,v
working file: atlas2/compass/layouts/uh_20x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:31; author: chip; state: Exp; Lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

=====

revision 2.1

date: 1995/02/08 16:05:43; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_21x1.ly,v
working file: atlas2/compass/layouts/uh_21x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:33; author: chip; state: Exp; Lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

=====

revision 2.1

date: 1995/02/08 16:05:44; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_22x1.ly,v
working file: atlas2/compass/layouts/uh_22x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2

date: 1995/02/08 19:17:34; author: chip; state: Exp; Lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

=====

revision 2.1

date: 1995/02/08 16:05:46; author: chip; state: Exp;

bx Feb 7-13 1995.log

periodic checkin of wed Feb 8 08:05:10 PST 1995

=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_23x1.ly,v
working file: atlas2/compass/layouts/uh_23x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:36; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:47; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_24x1.ly,v
working file: atlas2/compass/layouts/uh_24x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:37; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:49; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_25x1.ly,v
working file: atlas2/compass/layouts/uh_25x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:39; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:50; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

bx Feb 7-13 1995.log
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_26x1.ly,v
Working file: atlas2/compass/layouts/uh_26x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:40; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:52; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_27x1.ly,v
Working file: atlas2/compass/layouts/uh_27x1.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:42; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:53; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_28x1.ly,v
Working file: atlas2/compass/layouts/uh_28x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:44; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:56; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_29x1.ly,v
Working file: atlas2/compass/layouts/uh_29x1.ly
head: 2.3

bx Feb 7-13 1995.log

branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:47; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:58; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_2x1.ly,v
working file: atlas2/compass/layouts/uh_2x1.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:48; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:05:59; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_30x1.ly,v
working file: atlas2/compass/layouts/uh_30x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:50; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:00; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_3x1.ly,v
working file: atlas2/compass/layouts/uh_3x1.ly
head: 2.3
branch:
locks: strict
access list:

bx Feb 7-13 1995.log

```
symbolic names:
keyword substitution: kv
total revisions: 3;      selected revisions: 2
description:
-----
revision 2.2
date: 1995/02/08 19:17:52;  author: chip;  state: Exp;  Lines: +8 -5
periodic checkin of Wed Feb  8 11:17:06 PST 1995
-----
revision 2.1
date: 1995/02/08 16:06:03;  author: chip;  state: Exp;
periodic checkin of Wed Feb  8 08:05:10 PST 1995
=====
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_45x1.ly,v
working file: atlas2/compass/layouts/uh_45x1.ly
head: 2.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2;      selected revisions: 1
description:
-----
revision 2.1
date: 1995/02/09 22:36:21;  author: chip;  state: Exp;
periodic checkin of Thu Feb  9 14:36:18 PST 1995
=====
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_4x1.ly,v
working file: atlas2/compass/layouts/uh_4x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3;      selected revisions: 2
description:
-----
revision 2.2
date: 1995/02/08 19:17:54;  author: chip;  state: Exp;  Lines: +3 -3
periodic checkin of wed Feb  8 11:17:06 PST 1995
-----
revision 2.1
date: 1995/02/08 16:06:04;  author: chip;  state: Exp;
periodic checkin of Wed Feb  8 08:05:10 PST 1995
=====
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_5x1.ly,v
working file: atlas2/compass/layouts/uh_5x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3;      selected revisions: 2
description:
-----
revision 2.2
date: 1995/02/08 19:17:57;  author: chip;  state: Exp;  Lines: +3 -3
```

bx Feb 7-13 1995.log
periodic checkin of wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:06; author: chip; state: Exp;
periodic checkin of wed Feb 8 08:05:10 PST 1995

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_6x1.ly,v
working file: atlas2/compass/layouts/uh_6x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:17:59; author: chip; state: Exp; lines: +3 -3
periodic checkin of wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:08; author: chip; state: Exp;
periodic checkin of wed Feb 8 08:05:10 PST 1995

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_7x1.ly,v
working file: atlas2/compass/layouts/uh_7x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:18:02; author: chip; state: Exp; lines: +3 -3
periodic checkin of wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:09; author: chip; state: Exp;
periodic checkin of wed Feb 8 08:05:10 PST 1995

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_8x1.ly,v
working file: atlas2/compass/layouts/uh_8x1.ly
head: 2.4
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:18:06; author: chip; state: Exp; lines: +3 -3
periodic checkin of wed Feb 8 11:17:06 PST 1995

revision 2.1

bx Feb 7-13 1995.log
date: 1995/02/08 16:06:11; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_9x1.ly,v
working file: atlas2/compass/layouts/uh_9x1.ly
head: 2.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 19:18:08; author: chip; state: Exp; lines: +3 -3
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 2.1
date: 1995/02/08 16:06:12; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_grid.ly,v
working file: atlas2/compass/layouts/uh_grid.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 5; selected revisions: 2
description:

revision 2.2
date: 1995/02/08 18:14:10; author: chip; state: Exp; lines: +23 -4
periodic checkin of Wed Feb 8 10:14:08 PST 1995

revision 2.1
date: 1995/02/08 16:06:13; author: chip; state: Exp;
periodic checkin of Wed Feb 8 08:05:10 PST 1995
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/uh_pwr.ly,v
working file: atlas2/compass/layouts/uh_pwr.ly
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 2.2
date: 1995/02/11 00:07:36; author: chip; state: Exp; lines: +57 -4
periodic checkin of Fri Feb 10 16:07:34 PST 1995

revision 2.1
date: 1995/02/10 22:32:27; author: chip; state: Exp;
periodic checkin of Fri Feb 10 14:32:25 PST 1995
=====

bx Feb 7-13 1995.log

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/vlsi.atr,v
Working file: atlas2/compass/layouts/vlsi.atr
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:30; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/vlsi.idx,v
Working file: atlas2/compass/layouts/vlsi.idx
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.1
date: 1995/02/07 15:58:32; author: tom; state: Exp;
first cut at atlas compass area
=====

RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/vlsi.log,v
Working file: atlas2/compass/layouts/vlsi.log
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3575; selected revisions: 21
description:

revision 1.21
date: 1995/02/12 20:00:49; author: chip; state: Exp; lines: +2 -0
periodic checkin of Sun Feb 12 12:00:44 PST 1995

revision 1.20
date: 1995/02/11 01:48:36; author: chip; state: Exp; lines: +2 -0
periodic checkin of Fri Feb 10 17:48:33 PST 1995

revision 1.19
date: 1995/02/11 01:34:23; author: chip; state: Exp; lines: +1 -0
periodic checkin of Fri Feb 10 17:34:21 PST 1995

revision 1.18
date: 1995/02/11 00:07:37; author: chip; state: Exp; lines: +3 -0
periodic checkin of Fri Feb 10 16:07:34 PST 1995

revision 1.17
date: 1995/02/10 23:49:34; author: chip; state: Exp; lines: +2 -0
periodic checkin of Fri Feb 10 15:49:33 PST 1995

bx Feb 7-13 1995.log

revision 1.16
date: 1995/02/10 23:46:28; author: chip; state: Exp; lines: +2 -0
periodic checkin of Fri Feb 10 15:46:26 PST 1995

revision 1.15
date: 1995/02/10 22:32:29; author: chip; state: Exp; lines: +3 -0
periodic checkin of Fri Feb 10 14:32:25 PST 1995

revision 1.14
date: 1995/02/10 18:27:53; author: chip; state: Exp; lines: +1 -0
periodic checkin of Fri Feb 10 10:27:51 PST 1995

revision 1.13
date: 1995/02/09 23:10:50; author: chip; state: Exp; lines: +2 -0
periodic checkin of Thu Feb 9 15:10:46 PST 1995

revision 1.12
date: 1995/02/09 23:09:00; author: chip; state: Exp; lines: +1 -0
periodic checkin of Thu Feb 9 15:08:58 PST 1995

revision 1.11
date: 1995/02/09 22:36:24; author: chip; state: Exp; lines: +5 -0
periodic checkin of Thu Feb 9 14:36:18 PST 1995

revision 1.10
date: 1995/02/09 22:34:25; author: chip; state: Exp; lines: +3 -0
periodic checkin of Thu Feb 9 14:34:22 PST 1995

revision 1.9
date: 1995/02/09 21:28:30; author: chip; state: Exp; lines: +7 -0
periodic checkin of Thu Feb 9 13:28:21 PST 1995

revision 1.8
date: 1995/02/08 19:18:10; author: chip; state: Exp; lines: +87 -0
periodic checkin of Wed Feb 8 11:17:06 PST 1995

revision 1.7
date: 1995/02/08 18:21:10; author: chip; state: Exp; lines: +3 -0
periodic checkin of Wed Feb 8 10:21:07 PST 1995

revision 1.6
date: 1995/02/08 18:14:12; author: chip; state: Exp; lines: +2 -0
periodic checkin of Wed Feb 8 10:14:08 PST 1995

revision 1.5
date: 1995/02/08 17:06:36; author: chip; state: Exp; lines: +3 -0
periodic checkin of Wed Feb 8 09:06:30 PST 1995

revision 1.4
date: 1995/02/08 16:39:39; author: chip; state: Exp; lines: +2 -0
periodic checkin of Wed Feb 8 08:39:34 PST 1995

revision 1.3
date: 1995/02/08 16:06:15; author: chip; state: Exp; lines: +105 -0
periodic checkin of Wed Feb 8 08:05:10 PST 1995

revision 1.2
date: 1995/02/08 01:54:57; author: chip; state: Exp; lines: +5 -0
periodic checkin of Tue Feb 7 17:54:51 PST 1995

revision 1.1
date: 1995/02/07 15:58:34; author: tom; state: Exp;

bx Feb 7-13 1995.log

first cut at atlas compass area

```
=====
RCS file: /p/cvsroot/atlas2/compass/layouts/Attic/x1and4n160s.ly,v
Working file: atlas2/compass/layouts/x1and4n160s.ly
head: 2.7
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 7;      selected revisions: 2
description:
-----
revision 2.2
date: 1995/02/09 22:36:28;  author: chip;  state: Exp;  lines: +3 -3
periodic checkin of Thu Feb  9 14:36:18 PST 1995
-----
revision 2.1
date: 1995/02/09 22:34:27;  author: chip;  state: Exp;
periodic checkin of Thu Feb  9 14:34:22 PST 1995
=====
```

```
=====
RCS file: /p/cvsroot/atlas2/dcell/Attic/cache.dcell,v
Working file: atlas2/dcell/cache.dcell
head: 200.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 8;      selected revisions: 1
description:
-----
revision 1.3
date: 1995/02/11 19:30:40;  author: geert;  state: Exp;  lines: +8 -100
Changed from udr's to microns
=====
```

Geert

```
=====
RCS file: /p/cvsroot/atlas2/doc/BOM,v
Working file: atlas2/doc/BOM
head: 208.0
branch:
locks: strict
access list:
symbolic names:
  b4_rich: 203.0
keyword substitution: kv
total revisions: 101;      selected revisions: 13
description:
releasebom adding BOM
-----
revision 25.2
date: 1995/02/10 23:20:52;  author: bpw;  state: Exp;  lines: +2 -2
Release Target: atlas/doc
  ccr.ps
=====
```

Corrected some typos and added long line buffers.

```
=====
revision 25.1
date: 1995/02/10 22:46:08;  author: bpw;  state: Exp;  lines: +2 -2
=====
```

bx Feb 7-13 1995.log

Release Target: atlas/doc
ccr.ps

Moved input mux up half a tick.

revision 25.0
date: 1995/02/10 22:40:42; author: geert; state: Exp; lines: +1 -1
Release Target: atlas/doc

released new layout rules

Geert

revision 24.1
date: 1995/02/10 22:40:34; author: geert; state: Exp; lines: +2 -2
releasebom: File needs to be up-to-date to use commit -r

revision 24.0
date: 1995/02/09 23:16:21; author: wingard; state: Exp; lines: +1 -1
Release Target: atlas/doc

Clean up some files that we don't need anymore

revision 23.2
date: 1995/02/09 23:16:15; author: wingard; state: Exp; lines: +4 -6
releasebom: File needs to be up-to-date to use commit -r

revision 23.1
date: 1995/02/09 05:50:17; author: geert; state: Exp; lines: +2 -2
Release Target: atlas/doc
layout.ht

Minorfixes

Geert

revision 23.0
date: 1995/02/09 05:40:52; author: geert; state: Exp; lines: +1 -1
Release Target: atlas/doc

Added standard cell Layout guidelines

Geert

revision 22.1
date: 1995/02/09 05:40:43; author: geert; state: Exp; lines: +2 -2
releasebom: File needs to be up-to-date to use commit -r

revision 22.0
date: 1995/02/08 17:57:37; author: geert; state: Exp; lines: +1 -1
Release Target: atlas/doc

naming convection document

Geert

revision 21.1
date: 1995/02/08 17:57:31; author: geert; state: Exp; lines: +3 -2
releasebom: File needs to be up-to-date to use commit -r

revision 21.0
date: 1995/02/08 03:04:42; author: wingard; state: Exp; lines: +1 -1
Release Target: atlas/doc

bx Feb 7-13 1995.log

Update Makefiles and .ht files to correspond to new macro expansion method

revision 20.1

date: 1995/02/08 03:04:35; author: wingard; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas2/doc/Attic/Makefile,v
Working file: atlas2/doc/Makefile
head: 1.13
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 13; selected revisions: 1
description:

revision 1.13

date: 1995/02/08 02:36:02; author: wingard; state: Exp; lines: +2 -3
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas2/doc/CCR.ps,v
Working file: atlas2/doc/CCR.ps
head: 200.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 5; selected revisions: 3
description:

revision 23.3

date: 1995/02/10 23:20:30; author: bpw; state: Exp; lines: +188 -60
Corrected some typos and added long line buffers.

revision 23.2

date: 1995/02/10 22:44:55; author: bpw; state: Exp; lines: +500 -465
Moved mux up half a tick back to make it easier to make timing.

revision 23.1

date: 1995/02/09 23:15:32; author: wingard; state: Exp;
Clean up some files that we don't need anymore

RCS file: /p/cvsroot/atlas2/doc/cr.ht,v
Working file: atlas2/doc/cr.ht

head: 200.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 8; selected revisions: 3
description:

revision 5.6

date: 1995/02/09 23:15:34; author: wingard; state: Exp; lines: +3 -3

bx Feb 7-13 1995.log
Clean up some files that we don't need anymore

revision 5.5
date: 1995/02/08 17:57:01; author: geert; state: Exp; lines: +2 -1
merge with checked in stuff
Geert

revision 5.4
date: 1995/02/08 02:36:03; author: wingard; state: Exp; lines: +3 -3
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas2/doc/decisions.ht,v
Working file: atlas2/doc/decisions.ht
head: 200.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 10; selected revisions: 1
description:

revision 1.7
date: 1995/02/08 02:36:05; author: wingard; state: Exp; lines: +5 -5
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas2/doc/layout.ht,v
Working file: atlas2/doc/layout.ht
head: 200.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.0
keyword substitution: kv
total revisions: 20; selected revisions: 5
description:

revision 11.12
date: 1995/02/10 22:39:18; author: geert; state: Exp; lines: +4 -1
Added rule on standard cell layout

Geert

revision 11.11
date: 1995/02/09 23:15:36; author: wingard; state: Exp; lines: +6 -4
Clean up some files that we don't need anymore

revision 11.10
date: 1995/02/09 05:49:22; author: geert; state: Exp; lines: +2 -2
minor changes

Geert

revision 11.9
date: 1995/02/09 05:39:40; author: geert; state: Exp; lines: +12 -1
Added standard cell layout rules

Geetr

bx Feb 7-13 1995.log

revision 11.8
date: 1995/02/08 02:36:06; author: wingard; state: Exp; lines: +3 -3
Fix references to match new WebStar - refer to .ht rather than .html

RCS file: /p/cvsroot/atlas2/doc/Attic/name_conv.ht,v
Working file: atlas2/doc/name_conv.ht
head: 21.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 21.1
date: 1995/02/08 17:52:44; author: geert; state: Exp;
Naming Convention document

Geert

RCS file: /p/cvsroot/atlas2/ged/BOM,v
Working file: atlas2/ged/BOM
head: 207.61
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 207.57
keyword substitution: kv
total revisions: 1847; selected revisions: 2
description:

revision 4.2
date: 1995/02/09 19:00:39; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 4.1
date: 1995/02/09 01:47:15; author: ong; state: Exp; lines: +2 -2
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

RCS file: /p/cvsroot/atlas2/ged/xv/BOM,v
Working file: atlas2/ged/xv/BOM
head: 212.0
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 212.0
keyword substitution: kv
total revisions: 348; selected revisions: 4
description:
releasebom adding BOM

revision 5.0
date: 1995/02/09 19:00:29; author: ong; state: Exp; lines: +1 -1

bx Feb 7-13 1995.log

Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 4.1

date: 1995/02/09 19:00:22; author: ong; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r

revision 4.0

date: 1995/02/09 01:47:05; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 3.1

date: 1995/02/09 01:46:59; author: ong; state: Exp; lines: +22 -12
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas2/ged/xv/xvand2n/Attic/BOM,v
Working file: atlas2/ged/xv/xvand2n/BOM

head: 4.0

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 6; selected revisions: 4

description:

releasebom adding BOM

revision 3.0

date: 1995/02/09 18:57:05; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1

date: 1995/02/09 18:56:58; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0

date: 1995/02/09 01:11:09; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1

date: 1995/02/09 01:11:02; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas2/ged/xv/xvand2n/Attic/body.1.1,v
Working file: atlas2/ged/xv/xvand2n/body.1.1

head: 1.3

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 3; selected revisions: 2

description:

bx Feb 7-13 1995.log

revision 1.2
date: 1995/02/09 18:53:31; author: ong; state: Exp; lines: +4 -12
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.

revision 1.1
date: 1995/02/09 01:08:40; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand2n/Attic/body.2.1,v
Working file: atlas2/ged/xv/xvand2n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:53:34; author: ong; state: Exp; lines: +12 -20
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.

revision 1.1
date: 1995/02/09 01:08:42; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand2n/Attic/spice.1.1,v
Working file: atlas2/ged/xv/xvand2n/spice.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:08:44; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand2n/Attic/spice_cn.1.1,v
Working file: atlas2/ged/xv/xvand2n/spice_cn.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1

bx Feb 7-13 1995.log
date: 1995/02/09 01:08:46; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand3n/Attic/BOM,v
working file: atlas2/ged/xv/xvand3n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 4
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 18:57:26; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1
date: 1995/02/09 18:57:19; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0
date: 1995/02/09 01:11:26; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New Lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1
date: 1995/02/09 01:11:20; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas2/ged/xv/xvand3n/Attic/body.1.1,v
working file: atlas2/ged/xv/xvand3n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:53:55; author: ong; state: Exp; lines: +14 -14
Fixed Beta Parameter on Body.

revision 1.1
date: 1995/02/09 01:08:53; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand3n/Attic/body.2.1,v
working file: atlas2/ged/xv/xvand3n/body.2.1

bx Feb 7-13 1995.log

head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:53:56; author: ong; state: Exp; lines: +15 -15
Fixed Beta Parameter on Body.

revision 1.1
date: 1995/02/09 01:08:55; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand3n/Attic/spice.1.1,v
Working file: atlas2/ged/xv/xvand3n/spice.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:08:56; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand3n/Attic/spice_cn.1.1,v
Working file: atlas2/ged/xv/xvand3n/spice_cn.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:08:59; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvand4n/Attic/BOM,v
Working file: atlas2/ged/xv/xvand4n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:

bx Feb 7-13 1995.log

keyword substitution: kv
total revisions: 6; selected revisions: 2
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 01:11:49; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 2.1
date: 1995/02/09 01:11:42; author: ong; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r

=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvand4n/Attic/body.1.1,v
Working file: atlas2/ged/xv/xvand4n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 23:17:17; author: tom; state: Exp; lines: +18 -17
add PARMS properties to bodies of static gates

=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvand4n/Attic/body.2.1,v
Working file: atlas2/ged/xv/xvand4n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 23:17:20; author: tom; state: Exp; lines: +18 -17
add PARMS properties to bodies of static gates

=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvand4n/Attic/spice.1.1,v
Working file: atlas2/ged/xv/xvand4n/spice.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:07; author: ong; state: Exp; lines: +29 -29
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit

```
bx Feb 7-13 1995.log
Gave everyone credit for design of circuits (microlunatics)
=====
RCS file: /p/cvsroot/atlas2/ged/xv/xvand4n/Attic/spice_cn.1.1,v
working file: atlas2/ged/xv/xvand4n/spice_cn.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2;      selected revisions: 1
description:
-----
revision 1.2
date: 1995/02/09 01:09:09;  author: ong;  state: Exp;  lines: +18 -18
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvbufn/BOM,v
working file: atlas2/ged/xv/xvbufn/BOM
head: 204.0
branch:
locks: strict
access list:
symbolic names:
    b4_rich: 204.0
keyword substitution: kv
total revisions: 19;      selected revisions: 2
description:
releasebom adding BOM
-----
revision 3.0
date: 1995/02/09 01:12:09;  author: ong;  state: Exp;  lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n
-----
revision 2.1
date: 1995/02/09 01:12:03;  author: ong;  state: Exp;  lines: +4 -4
releasebom: File needs to be up-to-date to use commit -r
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvbufn/body.1.1,v
working file: atlas2/ged/xv/xvbufn/body.1.1
head: 200.4
branch:
locks: strict
access list:
symbolic names:
    b4_rich: 200.4
keyword substitution: kv
total revisions: 9;      selected revisions: 1
description:
-----
revision 1.2
date: 1995/02/08 23:17:24;  author: tom;  state: Exp;  lines: +3 -2
add PARMS properties to bodies of static gates
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvbufn/spice.1.1,v
```

bx Feb 7-13 1995.log
working file: atlas2/ged/xv/xvbufn/spice.1.1
head: 200.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.1
keyword substitution: kv
total revisions: 5; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:22; author: ong; state: Exp; lines: +145 -145
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvbufn/spice_cn.1.1,v
working file: atlas2/ged/xv/xvbufn/spice_cn.1.1
head: 200.1
branch:
locks: strict
access list:
symbolic names:
 b4_rich: 200.1
keyword substitution: kv
total revisions: 5; selected revisions: 1
description:

revision 1.2
date: 1995/02/09 01:09:23; author: ong; state: Exp; lines: +35 -35
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvor2n/Attic/BOM,v
working file: atlas2/ged/xv/xvor2n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 4
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 18:59:35; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1
date: 1995/02/09 18:59:28; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0
date: 1995/02/09 01:13:43; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

bx Feb 7-13 1995.log

New Lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1
date: 1995/02/09 01:13:36; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas2/ged/xv/xvor2n/Attic/body.1.1,v
Working file: atlas2/ged/xv/xvor2n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:54:15; author: ong; state: Exp; lines: +14 -14
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.

revision 1.1
date: 1995/02/09 01:09:30; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvor2n/Attic/body.2.1,v
Working file: atlas2/ged/xv/xvor2n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:54:17; author: ong; state: Exp; lines: +14 -14
Fixed Beta Parameter on Body; removed 3rd and 4th input pins.

revision 1.1
date: 1995/02/09 01:09:32; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvor2n/Attic/spice.1.1,v
Working file: atlas2/ged/xv/xvor2n/spice.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1

bx Feb 7-13 1995.log
date: 1995/02/09 01:09:33; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvor2n/Attic/spice_cn.1.1,v
working file: atlas2/ged/xv/xvor2n/spice_cn.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:09:35; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)

RCS file: /p/cvsroot/atlas2/ged/xv/xvor3n/Attic/BOM,v
working file: atlas2/ged/xv/xvor3n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 4
description:
releasebom adding BOM

revision 3.0
date: 1995/02/09 18:59:58; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

Fixed indecent bodies of xvand2/3n and xvor2/3n

revision 2.1
date: 1995/02/09 18:59:50; author: ong; state: Exp; lines: +3 -3
releasebom: File needs to be up-to-date to use commit -r

revision 2.0
date: 1995/02/09 01:14:00; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n

revision 1.1
date: 1995/02/09 01:13:53; author: ong; state: Exp;
releasebom: File needs to be up-to-date to use commit -r

RCS file: /p/cvsroot/atlas2/ged/xv/xvor3n/Attic/body.1.1,v
working file: atlas2/ged/xv/xvor3n/body.1.1
head: 1.3
branch:
locks: strict
access list:

bx Feb 7-13 1995.log

symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:54:56; author: ong; state: Exp; lines: +17 -17
Fixed Beta Parameter on Body.

revision 1.1
date: 1995/02/09 01:09:41; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvor3n/Attic/body.2.1,v
working file: atlas2/ged/xv/xvor3n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 2
description:

revision 1.2
date: 1995/02/09 18:54:59; author: ong; state: Exp; lines: +17 -17
Fixed Beta Parameter on Body.

revision 1.1
date: 1995/02/09 01:09:42; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvor3n/Attic/spice.1.1,v
working file: atlas2/ged/xv/xvor3n/spice.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 1.1
date: 1995/02/09 01:09:45; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvor3n/Attic/spice_cn.1.1,v
working file: atlas2/ged/xv/xvor3n/spice_cn.1.1
head: 1.1
branch:
locks: strict
access list:
symbolic names:

bx Feb 7-13 1995.log

```
keyword substitution: kv
total revisions: 1;      selected revisions: 1
description:
-----
revision 1.1
date: 1995/02/09 01:09:47; author: ong; state: Exp;
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====
RCS file: /p/cvsroot/atlas2/ged/xv/xvor4n/Attic/BOM,v
Working file: atlas2/ged/xv/xvor4n/BOM
head: 4.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6;      selected revisions: 2
description:
releasebom adding BOM
-----
revision 3.0
date: 1995/02/09 01:14:20; author: ong; state: Exp; lines: +1 -1
Release Target: atlas/ged/xv

New lobe schematics xvor2n xvor3n xvand2n xvand3n
-----
revision 2.1
date: 1995/02/09 01:14:14; author: ong; state: Exp; lines: +5 -5
releasebom: File needs to be up-to-date to use commit -r
=====
RCS file: /p/cvsroot/atlas2/ged/xv/xvor4n/Attic/body.1.1,v
Working file: atlas2/ged/xv/xvor4n/body.1.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3;      selected revisions: 1
description:
-----
revision 1.2
date: 1995/02/08 23:17:30; author: tom; state: Exp; lines: +1 -0
add PARMS properties to bodies of static gates
=====
RCS file: /p/cvsroot/atlas2/ged/xv/xvor4n/Attic/body.2.1,v
Working file: atlas2/ged/xv/xvor4n/body.2.1
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3;      selected revisions: 1
description:
-----
revision 1.2
date: 1995/02/08 23:17:31; author: tom; state: Exp; lines: +20 -19
```

```
          bx Feb 7-13 1995.log
add PARMs properties to bodies of static gates
=====
RCS file: /p/cvsroot/atlas2/ged/xv/xvor4n/Attic/spice.1.1,v
Working file: atlas2/ged/xv/xvor4n/spice.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2;      selected revisions: 1
description:
-----
revision 1.2
date: 1995/02/09 01:09:52; author: ong; state: Exp; lines: +50 -50
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/ged/xv/xvor4n/Attic/spice_cn.1.1,v
Working file: atlas2/ged/xv/xvor4n/spice_cn.1.1
head: 1.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2;      selected revisions: 1
description:
-----
revision 1.2
date: 1995/02/09 01:09:54; author: ong; state: Exp; lines: +25 -25
New Lobe Schematics: xv[and/or][23]n
Changed ordering of inputs s.t. msb=most slowest bit
Gave everyone credit for design of circuits (microlunatics)
=====

RCS file: /p/cvsroot/atlas2/leafgen/BOM,v
Working file: atlas2/leafgen/BOM
head: 238.0
branch:
locks: strict
access list:
symbolic names:
    b4_rich: 238.0
keyword substitution: kv
total revisions: 235;      selected revisions: 3
description:
releasebom adding BOM
-----
revision 4.0
date: 1995/02/11 00:59:44; author: brianl; state: Exp; lines: +1 -1
Release Target: atlas/leafgen

Generate Depend dependency and list file.
Add rules for xl schematic generation.
-----
revision 3.2
date: 1995/02/11 00:59:38; author: brianl; state: Exp; lines: +5 -6
releasebom: File needs to be up-to-date to use commit -r
=====
```

bx Feb 7-13 1995.log

revision 3.1
date: 1995/02/10 18:00:44; author: ong; state: Exp; lines: +6 -2
Release Target: atlas/leafgen
design_crit.ht
std_cells.ht

Adding Document Standard Cells Design Criterias

RCS file: /p/cvsroot/atlas2/leafgen/Makefile,v
Working file: atlas2/leafgen/Makefile
head: 200.18
branch:
locks: strict
access list:
symbolic names:
b4_rich: 200.18
keyword substitution: kv
total revisions: 50; selected revisions: 4
description:

revision 1.5
date: 1995/02/10 23:48:44; author: brianl; state: Exp; lines: +1 -2
parmsche -t option is now part of the default variable

revision 1.4
date: 1995/02/10 23:20:22; author: brianl; state: Exp; lines: +73 -3
Generate Depend dependency and list file.
Add rules for xl schematic generation.

revision 1.3
date: 1995/02/08 02:35:58; author: wingard; state: Exp; lines: +2 -2
Fix references to match new WebStar - refer to .ht rather than .html

revision 1.2
date: 1995/02/08 00:15:25; author: ong; state: Exp; lines: +2 -2
Added design criteria document. currently the only access is from CMOS STD
CELL document.

RCS file: /p/cvsroot/atlas2/leafgen/Attic/design_crit.ht,v
Working file: atlas2/leafgen/design_crit.ht
head: 3.4
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 2
description:

revision 3.2
date: 1995/02/10 17:54:46; author: ong; state: Exp; lines: +6 -6
New doc file descibing design criterias of standard cells

revision 3.1
date: 1995/02/08 00:15:27; author: ong; state: Exp;
Added design criteria document. currently the only access is from CMOS STD
CELL document.

RCS file: /p/cvsroot/atlas2/leafgen/Attic/makeDepend,v
Working file: atlas2/leafgen/makeDepend

bx Feb 7-13 1995.log

head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 15; selected revisions: 1
description:

revision 3.1
date: 1995/02/10 23:20:24; author: brianl; state: Exp;
Generate Depend dependency and list file.
Add rules for xl schematic generation.
=====

RCS file: /p/cvsroot/atlas2/leafgen/Attic/parmsche-tech.tcl,v
working file: atlas2/leafgen/parmsche-tech.tcl
head: 200.0
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 12; selected revisions: 1
description:

revision 3.1
date: 1995/02/10 20:17:21; author: tom; state: Exp;
new technology definition file
=====

RCS file: /p/cvsroot/atlas2/leafgen/Attic/std_cells.ht,v
working file: atlas2/leafgen/std_cells.ht
head: 1.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 1.2
date: 1995/02/08 00:15:29; author: ong; state: Exp; lines: +2 -2
Added design criteria document. currently the only access is from CMOS STD
CELL document.
=====

RCS file: /p/cvsroot/c_tests/plumhall/unixdst/envcshel,v
working file: c_tests/plumhall/unixdst/envcshel
head: 1.20
branch:
locks: strict
access list:
symbolic names:
 plumhall: 1.1.1.1
 plumhall5_0: 1.1.1.1
 PlumHall: 1.1.1
keyword substitution: kv
total revisions: 21; selected revisions: 3
description:

revision 1.18

```
                                bx Feb 7-13 1995.log
date: 1995/02/10 00:23:39;  author: claseman;  state: Exp;  lines: +1 -1
remove -target osf from CFLAGS
-----
revision 1.17
date: 1995/02/09 22:13:45;  author: claseman;  state: Exp;  lines: +1 -0
reset MUSEROOT to /a/soft/osf
-----
revision 1.16
date: 1995/02/09 00:13:30;  author: claseman;  state: Exp;  lines: +1 -1
specify target compiler
=====
RCS file: /p/cvsroot/c_tests/plumhall/unixdst/run_opt_tests,v
Working file: c_tests/plumhall/unixdst/run_opt_tests
head: 1.8
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 8;      selected revisions: 2
description:
-----
revision 1.8
date: 1995/02/10 00:49:42;  author: claseman;  state: Exp;  lines: +1 -1
remove -target osf from CFLAGS
-----
revision 1.7
date: 1995/02/09 00:12:42;  author: claseman;  state: Exp;  lines: +1 -1
specify target compiler
=====
```